

Figure 1 is an apparatus for the invention.

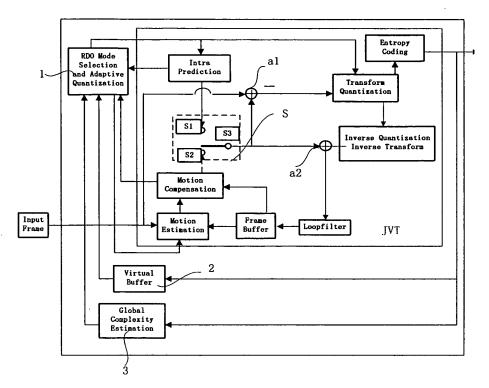


Figure 2 is an implementation of the invention on the JVT encoder